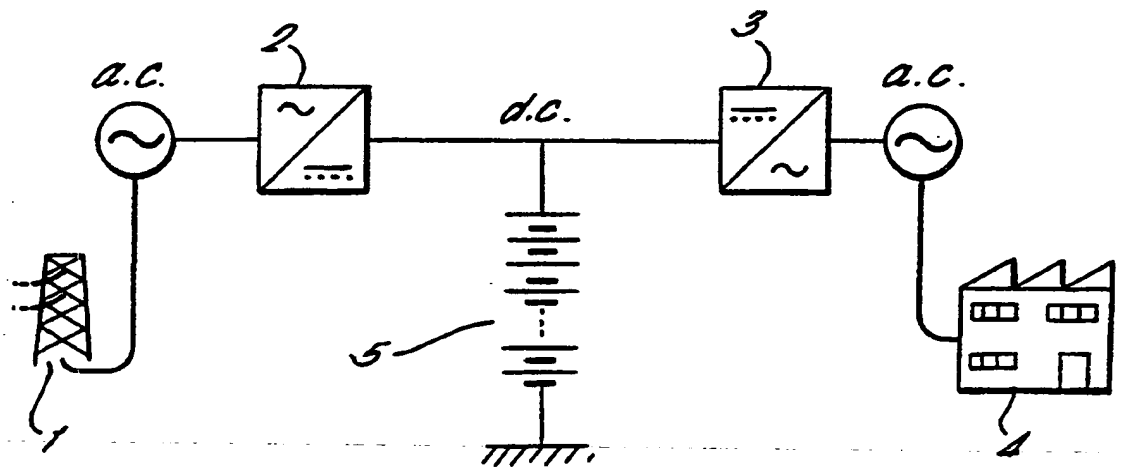
FIG. 1.



ACCEPTED (SEE PAPER "CET." 5/1/02)

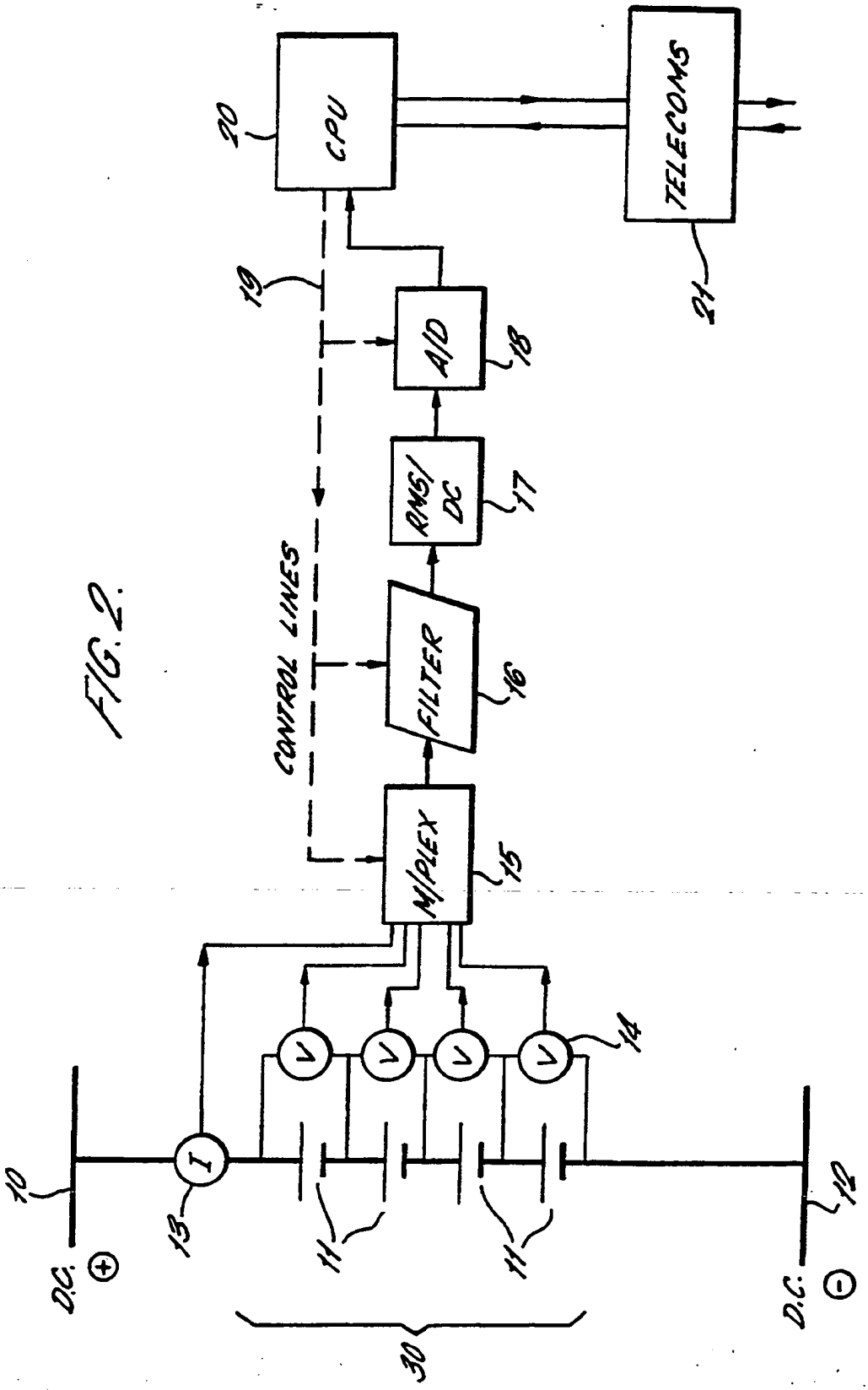

CPU
20
TELECOMS
21
19
A/D
18
RMS/ DC
17
CONTROL LINES
FILTER
16
M/PLEX
15
V
14
I
13
10
D.C. ⊕
11
11
12
D.C. ⊖
30

FIG. 2.

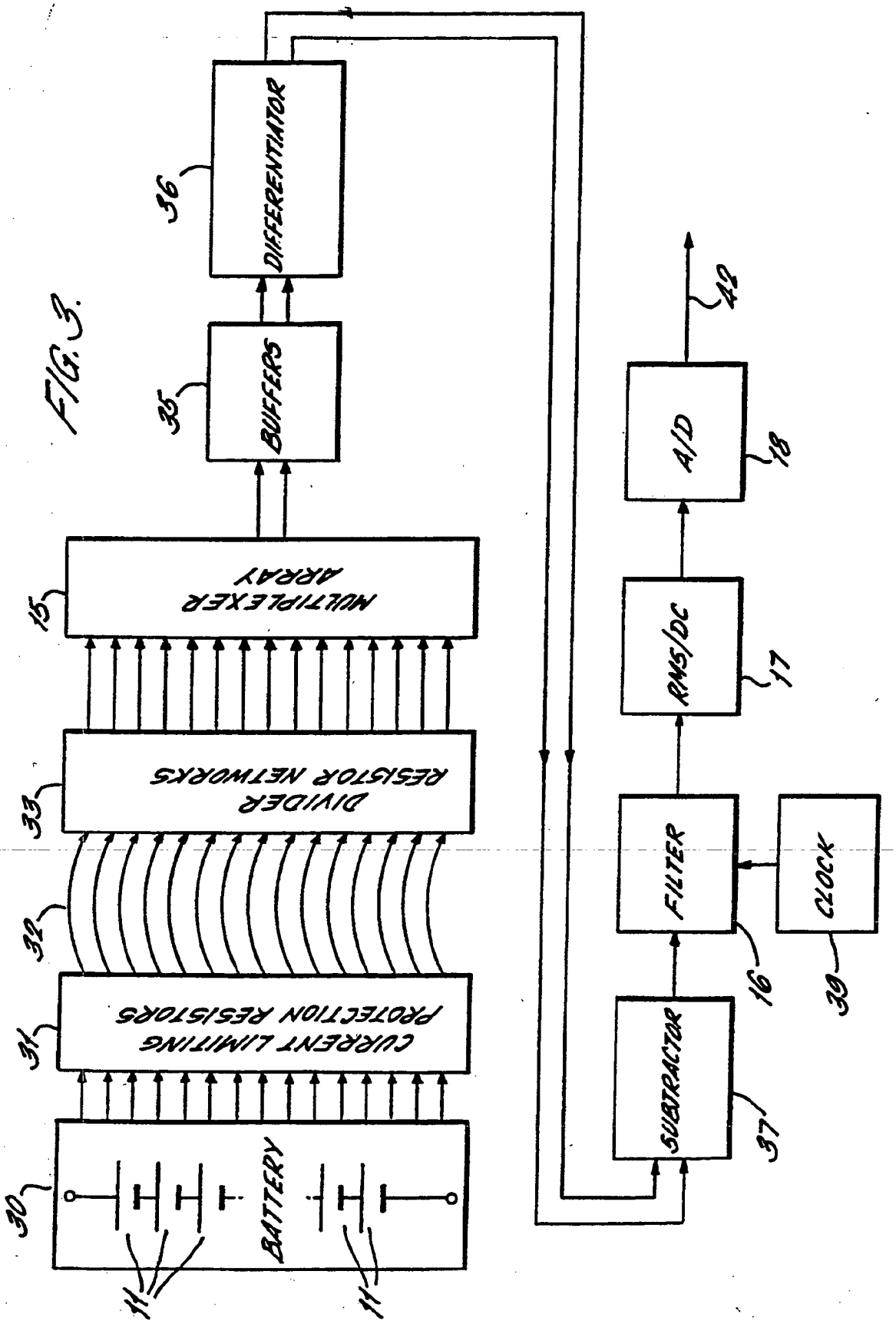

DIFFERENTIATOR
36
BUFFERS
35
MULTIPLEXER ARRAY
15
DIVIDER RESISTOR NETWORKS
33
CURRENT LIMITING PROTECTION RESISTORS
31
BATTERY
30
11
11
A/D
18
42
RMS/DC
17
FILTER
16
CLOCK
39
SUBTRACTOR
37

FIG. 3.